XA-9611 PATENT APPLICATION

MAR 1 9 2003 27

N THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hiroshi OKABE et al.

Appln. No.: 10/044,910 Group Art Unit: 2827

Filed: January 15, 2002 Examiner: T. Dinh

For: ELECTRONIC CIRCUIT EQUIPMENT USING MULTILAYER

CIRCUIT BOARD

TRANSMITTAL OF A CORRECTED FORMAL DRAWING

Commissioner for Patents Washington, D.C. 20231

Attn.: Drawing Review Section

Sir:

Attached is one (1) sheet of a formal drawing, corrected as required. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper to Deposit Account No. 50-1165.

Respectfully submitted,

By:

MWS:sjk

Mitchell W. Shapire Reg. No. 31,568

Miles & Stockbridge P.C. 1751 Pinnacle Drive Suite 500 McLean, Virginia 22102-3833 (703) 610-8652

March 19, 2003